

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

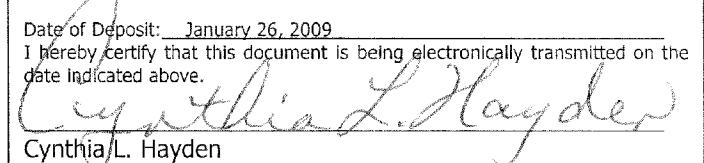
In re Applicant: § Art Unit: 2621
Louis Lippincott et al. §
Serial No.: 10/750,075 § Examiner: Chikaodili E. Anyikire
Filed: December 31, 2003 § Conf. No.: 2083
For: Motion Estimation Sum of all § Docket: ITL.1703US
Differences (SAD) Array Having § P17498
Reduced Semiconductor Die § Assignee: Intel Corporation
Area Consumption §

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed October 31, 2008, please amend the above-referenced patent application as follows:


Date of Deposit: January 26, 2009
I hereby certify that this document is being electronically transmitted on the
date indicated above.
Cynthia L. Hayden